EUROSOI-ULIS 2026
12th JOINT EUROSOI WORKSHOP AND INTERNATIONAL CONFERENCE ON ULTIMATE INTEGRATION ON SILICON

20 - 22 MAY 2026
Granada, Spain

Satellite events
IRDS & ISRDS
18-19th May, 2026
Granada, Spain

Scope

The conference brings together scientists and engineers to discuss SOI technology and advanced microelectronic devices.

We invite students and researchers from academia and industry to submit abstracts and join us in Granada to share knowledge and connect with colleagues.

Submission

https://easychair.org/conferences?conf=eurosoiulis2026

Deadline: ~~February 15~~ ~~March 1~~ March 8, 2026

https://eurosoiulis2026.ugr.es

eurosoiulis2026@ugr.es

Conference topics

1. Advanced SOI materials and structures, innovative SOI-like devices

2. Alternative transistor architectures (FDSOI, Nanowire, Nanosheet, FinFET, MuGFET, vertical MOSFET, FeFET and TFET, MEMS/NEMS, Beyond-CMOS)

3. New channel materials for CMOS (strained Si/Ge, III-V, carbon nanotubes; graphene and other 2D materials)

4. Properties of ultra-thin semiconductor films and buried oxides, defects, interface quality; thin gate dielectrics: high-κ and ferroelectric materials for switches and memory

5. New functionalities and innovative devices in the More than Moore domain: nanoelectronic sensors, biosensor devices, memrisors, neuromorphic computing devices, quantum computing devices, energy harvesting devices, RF devices, imagers, integrated photonics (on SOI), etc

6. Transport phenomena, compact modeling, device simulation, front- and back-end process simulation

7. CMOS scaling perspectives; device/circuit level performance evaluation; switches and memory scaling; three-dimensional integration of devices and circuits, heterogeneous integration

8. Advanced test structures and characterization techniques, parameter extraction, reliability and variability assessment techniques for new materials and novel devices

9. Memory devices, memristors, hardware for unconventional computing such as neuromorphic, in-memory, in-sensor and quantum computing

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Satellite events

ISRDS & IRDS EVENT
Draft programme

📍 **18-19th May, 2026**
Granada, Spain
FREE EVENT

MON. 18 — **Day 1 – IRDS**
Annual update of the roadmap summaries

- IRDS Introduction
- Yield Enhancement (YE)
- Applications, Systems, & Architecture (ASA)
- Autonomous Machine Computing (AMC)
- Metrology (MET)
- More than Moore (MtM)
- Cryogenic Electronics & Quantum Information Processing (CEQIP)
- Environment, Safety, Health & Sustainability (ESH/S) and Environmental Sustainability of Semiconductor Fabrication (ESSF)
- More Moore (MM)
- Packaging Integration (PI)
- Beyond CMOS & Emerging Materials (BC &EMI)

TUE. 19 — **Day 2 – ISRDS**
International Symposium on Roadmapping Devices and Systems

New directions in Computing

New directions in materials and devices for Computing

- Steep Slope Transistors
- Neuromorphic Computing
- Quantum materials and technologies for Computing
- Si based Devices for Cryogenic Computing
- New computational approaches based on NEMS
- 2D Devices for Computing

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Important dates

- Abstract submission deadline: ~~February 15, 2026~~ ~~March 1, 2026~~ March 8, 2026
- Notification of acceptance: March 31, 2026
- Registration opening: March 1, 2026
- Early Bird Registration Deadline: April 20, 2026

Invited Speakers

- **Hanwool Yeon**, Gwangju Institute of Science and Technology
- **John D. Cressler**, Georgia Institute of Technology
- **Saptarshi Das**, Pennsylvania State University
- **Tianchun Ye**, University of Chinese Academy of Sciences
- **Andrea Capasso**, International Iberian Nanotechnology Laboratory

Abstract Submission instructions

The abstract is limited to **two pages** of text. Please **underline the presenting author** in the author list and mark the corresponding author with an asterisk (*) as superscript.

- Prepare your abstract in English according to the guidelines in the template: Template
- Submit your abstract via EasyChair here: https://easychair.org/conferences?conf=eurosoiulis2026

Publication

Selected papers will be published as 4-page letters in a Special Issue of Solid-State Electronics (Elsevier)

Awards

- The *Androula Nassiopoulou Best Paper Award* will be attributed by the SINANO Institute
- A best poster award will be attributed by Solid-State Electronics journal (Elsevier)

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Venue

Assembly Hall of the Faculty of Medicine, Tower B, Floor 1
University of Granada. PTS Parque Tecnológico de la Salud
Avda. Doctor Jesús Candel Fábregas 11, 18016 Granada

https://medicina.ugr.es/

Accommodation & Transportation Information

A list of recommended hotels has been prepared, located near the conference venue and along the metro line connecting the university area with the city center.

Key points:

◆ All recommended hotels are well connected by public transportation.

◆ The metro offers a simple and efficient way to reach the conference venue and move around the city.

◆ We recommend choosing accommodation near one of the indicated metro stations shown on the conference map.

◆ The full hotel list and map are available on the conference website

Social events

◆ 20 May – **Cultural Visit** and **Welcome Cocktail** at **Carmen de la Victoria**
◆ 21 May – **Gala Dinner**

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